

FIG. 1

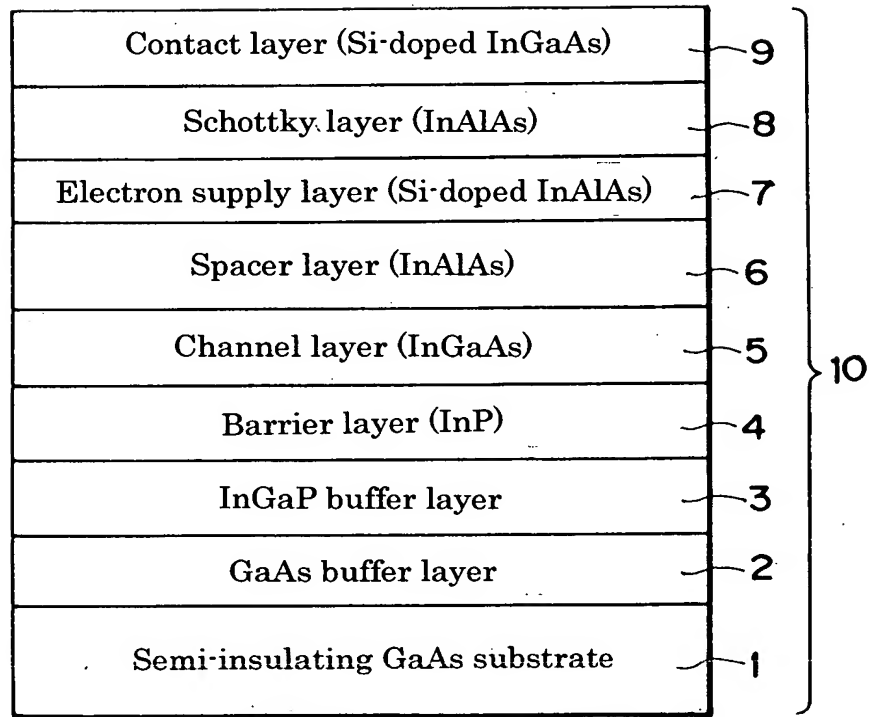


FIG. 2

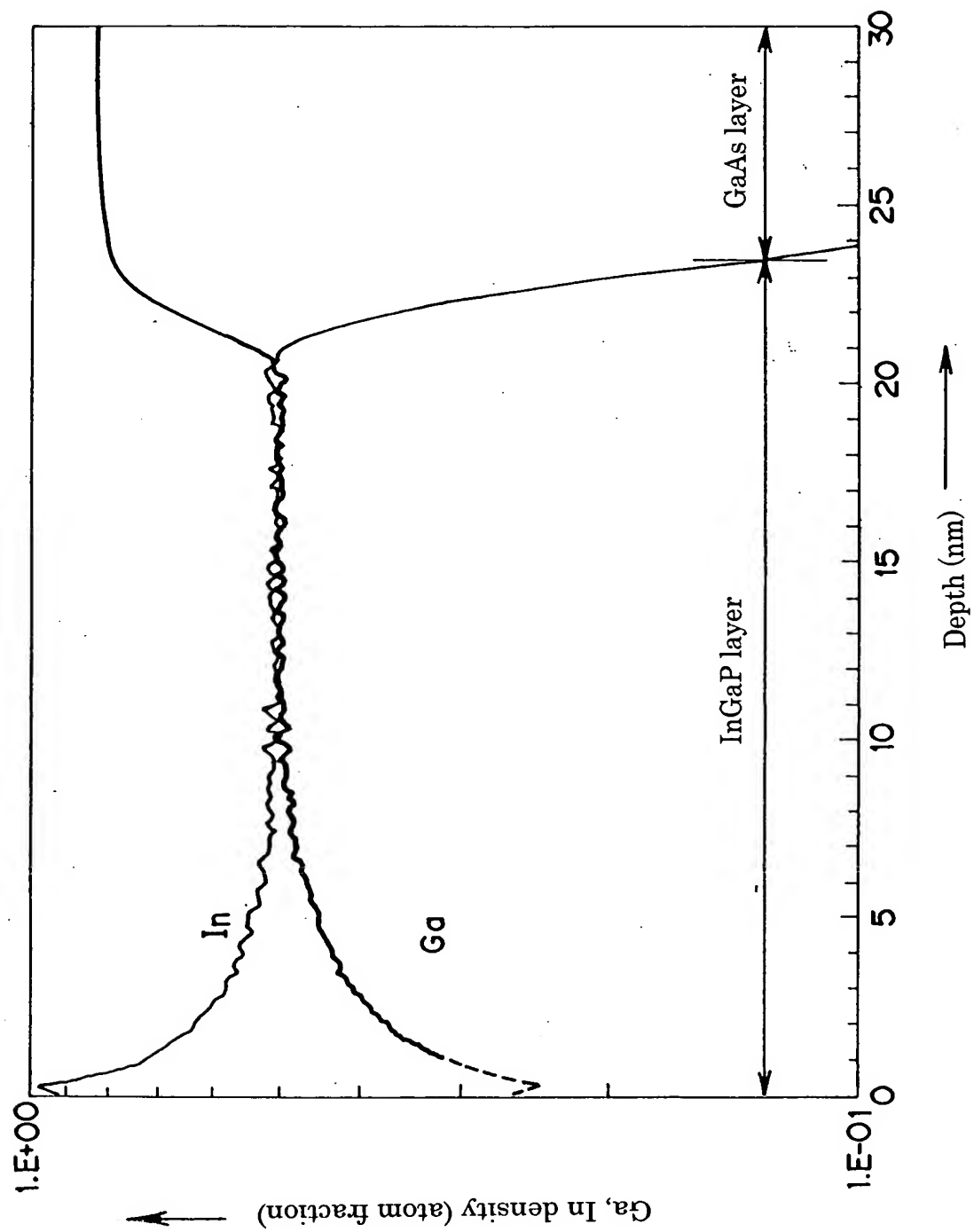




FIG. 3

Contact layer (Si-doped InGaAs)	9	} 20
Schottky layer (InAlAs)	8	
Electron supply layer (Si-doped InAlAs)	7	
Spacer layer (InAlAs)	6	
Channel layer (InGaAs)	5	
Barrier layer (InP)	4	}
InP buffer layer	4A	
InGaP buffer layer	3	
GaAs buffer layer	2	
Semi-insulating GaAs substrate	1	